

JDW 1011-64537-01

01/24/07

JAN 24 2007

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To: EXAMINER THOMAS H. STEVENS

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Total No. Pages: 5 including this cover sheet

Message: Transmitted herewith is an Applicant Initiated Interview Request Form for the below-identified application. If you do not receive all pages or if you have problems receiving transmittal, please call Justin D. Wagner at (503) 595-5300.

In re application of: Bailey et al.

Application No. 09/883,836

Filed: June 17, 2001

Confirmation No. 9966

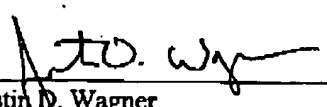
For: COHERENT STATE AMONG MULTIPLE
SIMULATION MODELS IN AN EDA
SIMULATION ENVIRONMENT

Examiner: Thomas H. Stevens

Art Unit: 2123

Attorney Reference No. 1011-64537-01

☒ Applicant Initiated Interview Request Form (PTOL-413A) with a Continuation Sheet.


Justin D. Wagner
Registration No. 54,519

January 24, 2007
Date

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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Applicant Initiated Interview Request Form

Application No. 09/883,836 First Named Applicant: Bailey
Examiner: Thomas H. Stevens Art Unit: 2123 Status of Application: Pending

Tentative Participants:

(1) Ex. Stevens (2) Justin Wagner
(3) _____ (4) _____

Proposed Date of Interview: 1/25/07 Proposed Time: 3:00 Eastern (AM/PM)

Type of Interview Requested:

(1) ☒ Telephonic (2) ☐ Personal (3) ☐ Video Conference

Exhibit To Be Shown or Demonstrated: ☐ YES ☒ NO

If yes, provide brief description: _____

Issues To Be Discussed

Issues (Rej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1) <u>Rej.</u>	<u>cls 1-36</u>	<u>Madden, Paterson, Ravichandran, Bailey</u>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(2) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(3) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(4) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☒ Continuation Sheet Attached

Brief Description of Arguments to be Presented:

Please see Continuation sheet

An interview was conducted on the above-identified application on _____.

NOTE:

This form should be completed by applicant and submitted to the examiner in advance of the interview (see MPEP § 713.01).

This application will not be delayed from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of the substance of this interview (37 CFR 1.133(b)) as soon as possible.

(Applicant/Applicant's Representative Signature)

(Examiner/SPE Signature)

This collection of information is required by 37 CFR 1.133. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 21 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

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PATENTAttorney Reference Number 1011-64537-01
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APPLICANT INITIATED INTERVIEW REQUEST FORM CONTINUATION SHEET

Claims 1-5 and 7

Applicant respectfully submits that claims 1-5 and 7 are patentable over U.S. Patent No. 5,119,483 to Madden et al. ("Madden"), U.S. Patent No. 6,983,237 to Paterson et al. ("Paterson"), and U.S. Patent No. 5,966,537 to Ravichandran ("Ravichandran") because, for example, the references do not teach or suggest all the claim limitations of independent claim 1. For example, none of the references, individually or in combination, teaches or suggests identifying state information comprising a transfer from a first simulation model in a simulation environment, said transfer being directed to a second simulation model in a circuit design being simulated in the simulation environment, as recited in independent claim 1.

Applicant respectfully submits that dependent claims 2-5 and 7 depend directly or indirectly from independent claim 1 and are allowable over Madden, Paterson, and Ravichandran for at least the reasons recited above in support of their parent claim 1. They are also independently patentable.

Claims 6, 8-22, 24-31

Applicant respectfully submits that dependent claims 6, 8-22, 24-31 are patentable over Madden, Paterson, Ravichandran, and Bailey et al., "Hardware/Software Co-Simulation Strategies for the Future" ("Bailey"). These dependent claims depend directly or indirectly from independent claim 1 and are allowable for at least the reasons recited above with respect to their parent claim 1. Moreover, claims 6, 8-22, and 24-31 recite combinations of features that are independently patentable.

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PATENTAttorney Reference Number 1011-64537-01
Application Number 09/883,836Claims 32-34

Applicant respectfully submits that claims 32-34 are patentable over Madden, Paterson, and Bailey because, for example, the references do not teach or suggest all the claim limitations of independent claim 32. For example, none of the references, individually or in combination, teaches or suggests reading state information from a first simulation model in a simulation environment when a simulation domain of the first simulation model is deactivated or writing the state information to a second simulation model in the simulation environment prior to activation of a simulation domain of the second simulation model, said first simulation model and said second simulation model representing different versions of a same functionality in a circuit design being simulated, as recited in independent claim 32.

Applicant respectfully submits that dependent claims 33 and 34 depend directly or indirectly from independent claim 32 and are allowable over Madden, Paterson, and Bailey for at least the reasons recited above in support of their parent claim 32. They are also independently patentable.

Claim 35

Applicant respectfully submits that claim 35 is patentable over Madden, Paterson, and Bailey because, for example, the references do not teach or suggest all the claim limitations of independent claim 35. For example, none of the references, individually or in combination, teaches or suggests identifying state information comprising a transfer from a first simulation model in a simulation environment, said transfer being directed to a second simulation model in a circuit design being simulated in the simulation environment, as recited in independent claim 35.

Claim 36

Applicant respectfully submits that claim 36 is patentable over Madden, Paterson, and Bailey because, for example, the references do not teach or suggest all the claim limitations of independent claim 36. For example, none of the references, individually or in combination, teaches or suggests reading state information from a first simulation model in a simulation environment when a simulation domain of the first simulation model is deactivated or writing the state information to a second simulation model in the simulation environment prior to activation

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of a simulation domain of the second simulation model, said first simulation model and said second simulation model representing different versions of a same functionality in a circuit design being simulated, as recited in independent claim 36.